

Amendments to the Specification:

Please replace the paragraph on page 2, lines 7-14 with the following rewritten version:

In order to achieve the solution, a thin film transistor array panel according the present invention includes: an insulating substrate; a gate wire formed on the insulating substrate and including a plurality of gate portions and a gate connection connecting the gate portions; a data wire insulated from the gate wire and intersecting the gatedata wire; a thin film transistor connected to the gate wire and the data wire; and a pixel electrode connected to the thin film transistor. The data wire includes a plurality of data portions and a data connection connecting the data portions.